

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	17480	(substrate and ("first region" and "second region"))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/07 14:11
L2	8146	(substrate and ("first region" and "second region")) and gate\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/07 14:17
L3	33657481	(substrate and ("first region" and "second region")) and gate\$4 a	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/07 14:17
L4	18	(substrate and ("first region" and "second region")) and gate\$4 and dielectric and conductive and cap and spacer and buffer and (open\$4 or via or hole) and polysilicon	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/07 14:18